

J. B. INSTITUTE OF ENGINEERING & TECHNOLOGY

Regulation R24

UGC Autonomous **EXAMINATION BRANCH**

TIME TABLE OF I M. TECH - II Semester (2024 Batch) – II MID EXAMINATIONS, AUGUST-2025 TIMINGS: Fore Noon (FN) - 10:00 AM to 11:20 AM

Branch	04-08-2025 FN	05-08-2025 FN	06-08-2025 FN	07-08-2025 FN	08-08-2025 FN	11-08-2025 FN
Electrical Power Systems	ADVANCED POWER SYSTEM PROTECTION	POWER SYSTEM OPERATION AND DEREGULATION	PE-III HVDC Transmission	PE-IV Voltage Stability	RESEARCH METHODOLOGY AND IPR	PROFESSIONAL VALUE AND ETHICS
CAD/CAM	MANUFACTURING SYSTEMS: SIMULATION MODELING& ANALYSIS	ADVANCED FINITE ELEMENT METHODS	PE-III Special Manufacturing Process	PE-IV Design For Manufacturing and Assembly	RESEARCH METHODOLOGY AND IPR	PROFESSIONAL VALUE AND ETHICS
VLSI System Design	ANALOG CMOS IC DESIGN	LOW POWER VLSI DESIGN	PE-III SOC Design	PE-IV Image and Video Processing	RESEARCH METHODOLOGY AND IPR	PROFESSIONAL VALUE AND ETHICS
Computer Science & Engineering	ADVANCED ALGORITHMS	MACHINE LEARNING	PE-III Big Data Analytics	PE-IV Internet of Things	RESEARCH METHODOLOGY AND IPR	PROFESSIONAL VALUE AND ETHICS

Date: 19-07-2025

Controller of Examinations

J.B. INSTITUTE OF
ENGINEERING & TECHNOLOGY
Bhasker Nagar, Yenkapally (V),
Moinabad Will Wast-500 075.
Chief Controller of Examinations

Note: Any Omissions Or Clashes In This Time Table May Please Be Informed To The Controller Of Examinations Immediately.