

J. B. INSTITUTE OF ENGINEERING & TECHNOLOGY

UGC Autonomous

TALL TION DD ANG

EXAMINATION BRANCH
TIME TABLE OF I M. TECH - II Semester (2023 & 2022 Batches) – SUPPLY EXAMINATIONS, AUGUST 2025

TIMINGS: FN - 10:00 AM to 01:00 PM

Branch	18-08-2025	20-08-2025	22-08-2025	25-08-2025	28-08-2025
	FN	FN	FN	FN	FN
Structural Engineering	THEORY OF PLATES AND SHELLS	STRUCTURAL DYNAMICS	PE-III Low Cost Housing Techniques	PE-IV Energy Audit, Sustainable Materials & Green Buildings	RESEARCH METHODOLOGY AND IPR
Electrical Power Systems	ADVANCED POWER SYSTEM PROTECTION	POWER SYSTEM OPERATION AND DEREGULATION	PE-III HVDC Transmission	PE-IV Voltage Stability	RESEARCH METHODOLOGY AND IPR
CAD /CAM	MANUFACTURING SYSTEMS: SIMULATION MODELING& ANALYSIS	ADVANCED FINITE ELEMENT METHODS	PE-III Special Manufacturing Process	PE-IV Design For Manufacturing and Assembly	RESEARCH METHODOLOGY AND IPR
VLSI System Design	ANALOG CMOS IC DESIGN	LOW POWER VLSI DESIGN	PE-III SOC Design	PE-IV Image and Video Processing	RESEARCH METHODOLOGY AND IPR
Computer Science & Engineering	ANDROID APPLICATION DEVELOPMENT	NETWORK PROGRAMMING	PE-III Data Analytics	PE-IV Computer Vision/ Distributed Data Bases	RESEARCH METHODOLOGY AND IPR

Date: 28-07-2025

Note: Any Omissions Or Clashes In This Time Table May Please Be Informed To The Controller Of Examinations Immediately.

Controller of Examinations

Chief Controller of Examinations ENGINEERING & TECHNOLOGIS Bhaskar Nagar, Yenkapally (V), Moinabad (M), R.R. Dist.-500 075.

Regulation

R22



J. B. INSTITUTE OF ENGINEERING & TECHNOLOGY

UGC Autonomous

EXAMINATION BRANCH

TIME TABLE OF I M. TECH - II Semester (2021 Batch) – SUPPLY EXAMINATIONS, AUGUST 2025

TIMINGS: FN - 10:00 AM to 01:00 PM

Branch	18-08-2025	20-08-2025	22-08-2025	25-08-2025	28-08-2025	30-08-2025
	FN	FN	FN	FN	FN	FN
Structural Engineering	Theory of Plates and Shells	Structural Dynamics	Precast Concrete and Pre-Engineered Buildings	Energy Audit, Sustainable Materials & Green Buildings	Research Methodology & Intellectual Property Rights	Personality Development & Professional Values
Electrical Power Systems	Advanced Power System Protection	Power System Operation and Deregulation	HVDC Transmission	Voltage Stability	Research Methodology & Intellectual Property Rights	Personality Development & Professional Values
CAD/CAM	Manufacturing Systems: Simulation Modeling & Analysis	Advanced Finite Element Methods	Special Manufacturing Process	Design For Manufacturing and Assembly	Research Methodology & Intellectual Property Rights	Personality Development & Professional Values
VLSI System Design	Analog CMOS IC Design	Low Power VLSI Design	SOC Design	Image and Video Processing/Design for Testability	Research Methodology & Intellectual Property Rights	Personality Development & Professional Values
Computer Science & Engineering	Advanced Algorithms	Network Programming	Data Analytics	Distributed Databases	Research Methodology & Intellectual Property Rights	Personality Development & Professional Values

Date: 28-07-2025

Note: Any Omissions Or Clashes In This Time Table May Please Be Informed To The Controller Of Examinations Immediately.

Controller of Examinations

Chief Controller of Examinations V,
Bhaskar Nagar,
Moinabad (M), R.R. Dist.-500 075.

Regulation

R20