



J. B. INSTITUTE OF ENGINEERING & TECHNOLOGY

UGC Autonomous
EXAMINATION BRANCH

Regulation
R24

TIME TABLE OF I M. TECH - II Semester (2024 Batch) SUPPLEMENTARY EXAMINATIONS, FEBRUARY 2026 TIMINGS: FN - 10:00 AM to 01:00 PM

Branch	24-02-2026	26-02-2026	28-02-2026	04-03-2026	06-03-2026
	FN	FN	FN	FN	FN
Structural Engineering	THEORY OF PLATES AND SHELLS	STRUCTURAL DYNAMICS	PE-III Low Cost Housing Techniques	PE-IV Energy Audit, Sustainable Materials & Green Buildings	RESEARCH METHODOLOGY AND IPR
Electrical Power Systems	ADVANCED POWER SYSTEM PROTECTION	POWER SYSTEM OPERATION AND DEREGULATION	PE-III HVDC Transmission	PE-IV Voltage Stability	RESEARCH METHODOLOGY AND IPR
CAD /CAM	MANUFACTURING SYSTEMS: SIMULATION MODELING& ANALYSIS	ADVANCED FINITE ELEMENT METHODS	PE-III Special Manufacturing Process	PE-IV Design For Manufacturing and Assembly	RESEARCH METHODOLOGY AND IPR
VLSI System Design	ANALOG CMOS IC DESIGN	LOW POWER VLSI DESIGN	PE-III SOC Design	PE-IV Image and Video Processing	RESEARCH METHODOLOGY AND IPR
Computer Science & Engineering	ADVANCED ALGORITHMS	MACHINE LEARNING	PE-III Big Data Analytics	PE-IV Internet of Things	RESEARCH METHODOLOGY AND IPR

Date: 04-02-2026.


Controller of Examinations

Note: Any Omissions Or Clashes In This Time Table May Please Be Informed To The Controller Of Examinations Immediately.


Chief Controller of Examinations

PRINCIPAL
J. B. INSTITUTE OF
ENGINEERING & TECHNOLOGY
R. Dist-500 075.



J. B. INSTITUTE OF ENGINEERING & TECHNOLOGY

UGC Autonomous

EXAMINATION BRANCH

Regulation
R22


TIME TABLE OF I M. TECH - II Semester (2023 & 2022 Batches) – SUPPLY EXAMINATIONS, FEBRUARY 2026


TIMINGS: FN - 10:00 AM to 01:00 PM

Branch	24-02-2026	26-02-2026	28-02-2026	04-03-2026	06-03-2026
	FN	FN	FN	FN	FN
Structural Engineering	THEORY OF PLATES AND SHELLS	STRUCTURAL DYNAMICS	PE-III Low Cost Housing Techniques	PE-IV Energy Audit, Sustainable Materials & Green Buildings	RESEARCH METHODOLOGY AND IPR
Electrical Power Systems	ADVANCED POWER SYSTEM PROTECTION	POWER SYSTEM OPERATION AND DEREGULATION	PE-III HVDC Transmission	PE-IV Voltage Stability	RESEARCH METHODOLOGY AND IPR
CAD /CAM	MANUFACTURING SYSTEMS: SIMULATION MODELING& ANALYSIS	ADVANCED FINITE ELEMENT METHODS	PE-III Special Manufacturing Process	PE-IV Design For Manufacturing and Assembly	RESEARCH METHODOLOGY AND IPR
VLSI System Design	ANALOG CMOS IC DESIGN	LOW POWER VLSI DESIGN	PE-III SOC Design	PE-IV Image and Video Processing	RESEARCH METHODOLOGY AND IPR
Computer Science & Engineering	ANDROID APPLICATION DEVELOPMENT	NETWORK PROGRAMMING	PE-III Data Analytics	PE-IV Computer Vision/ Distributed Data Bases	RESEARCH METHODOLOGY AND IPR

Date: 04-02-2026

Note: Any Omissions Or Clashes In This Time Table May Please Be Informed To The Controller Of Examinations Immediately.


Controller of Examinations


PRINCIPAL
J. B. INSTITUTE OF
ENGINEERING & TECHNOLOGY
Chief Controller of Examinations
Bhaskar Nagar, Yenkapally (V),
Molnabad (M), R.R. Dist.-500 075.