



J. B. INSTITUTE OF ENGINEERING & TECHNOLOGY

UGC Autonomous
EXAMINATION BRANCH

Regulation
R25

TIME TABLE OF I M. TECH - II Semester (2025 Batch) – I MID EXAMINATIONS, MAY 2026

TIMINGS: Fore Noon (FN) - 10:00 AM to 11:20 AM
12:00

Branch	04-05-2026	05-05-2026	06-05-2026	07-05-2026
	FN	FN	FN	FN
Structural Engineering	FINITE ELEMENT ANALYSIS	STRUCTURAL DYNAMICS	ADVANCED STRUCTURAL STEEL DESIGN	STRUCTURAL HEALTH MONITORING
Electrical Power Systems	DIGITAL PROTECTION OF POWER SYSTEM	POWER SYSTEM DYNAMICS	POWER QUALITY IMPROVEMENT TECHNIQUES	ELECTRIC VEHICLE CHARGING TECHNIQUES
CAD /CAM	COMPUTER INTEGRATED MANUFACTURING	MANUFACTURING SYSTEMS: SIMULATION MODELING & ANALYSIS	SMART MANUFACTURING	DESIGN AND MANUFACTURING
VLSI System Design	MIXED SIGNAL IC DESIGN	FPGA BASED SYSTEM DESIGN	VLSI TEST AND TESTABILITY	LOW POWER VLSI DESIGN
Computer Science & Engineering	ADVANCED ALGORITHMS	ADVANCED COMPUTER ARCHITECTURE	CYBER SECURITY	GENERATIVE AI

Date: 20-04-2026


Controller of Examinations

Note: Any Omissions Or Clashes In This Time Table May Please Be Informed To The Controller Of Examinations Immediately


Chief Controller of Examinations

J. B. INSTITUTE OF
ENGINEERING & TECHNOLOGY
Bhaskar Nagar, Yenkapally (V),
Moinabad (M), R.R. Dist.-500 075.