



J. B. INSTITUTE OF ENGINEERING & TECHNOLOGY

UGC Autonomous
EXAMINATION BRANCH

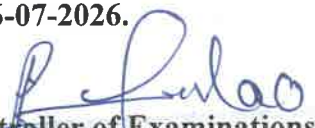
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TIME TABLE OF I M. TECH - II Semester (2025 Batch) – REGULAR EXAMINATIONS, AUGUST 2026

TIMINGS: Fore Noon (FN) - 10:00 AM to 01:00 PM

Branch	03-08-2026	05-08-2026	07-08-2026	11-08-2026
	FN	FN	FN	FN
Structural Engineering	FINITE ELEMENT ANALYSIS	STRUCTURAL DYNAMICS	ADVANCED STRUCTURAL STEEL DESIGN	STRUCTURAL HEALTH MONITORING
Electrical Power Systems	DIGITAL PROTECTION OF POWER SYSTEM	POWER SYSTEM DYNAMICS	POWER QUALITY IMPROVEMENT TECHNIQUES	ELECTRIC VEHICLE CHARGING TECHNIQUES
CAD /CAM	COMPUTER INTEGRATED MANUFACTURING	MANUFACTURING SYSTEMS: SIMULATION MODELING& ANALYSIS	SMART MANUFACTURING	DESIGN AND MANUFACTURING
VLSI System Design	MIXED SIGNAL IC DESIGN	FPGA BASED SYSTEM DESIGN	VLSI TEST AND TESTABILITY	LOW POWER VLSI DESIGN
Computer Science & Engineering	ADVANCED ALGORITHMS	ADVANCED COMPUTER ARCHITECTURE	CYBER SECURITY	GENERATIVE AI

Date: 06-07-2026.


Controller of Examinations

Controller of Examinations
J.B. Institute of Engineering & Technology
Yenkapally (V), Moinabad (M), TS-500 075.


Chief Controller of Examinations

J.B. INSTITUTE OF
ENGINEERING & TECHNOLOGY
Bhaskar Nagar, Yenkapally (V),
Moinabad (M), R.R. Dist-500 075.

Note: Any Omissions Or Clashes In This Time Table May Please Be Informed To The Controller Of Examinations Immediately.